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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

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| APPL NUM 10090826 | FILING DATE 03/06/2002 | CLASS 365 | SUBCLASS 137 | GAU 2848 | EXAMINER <i>M. Kim Namazi</i> |
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**APPLICANTS: Saruwatari Toshiaki; Fujita Atsushi;
2188

**CONTINUING DATA VERIFIED: *none*

** FOREIGN APPLICATIONS VERIFIED: *Yes*

JAPAN 2001-194810 06/27/2001

| | | |
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| PG-PUB | DO NOT PUBLISH <input type="checkbox"/> | RESCIND <input type="checkbox"/> |
| Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no | ATTORNEY DOCKET NO | |
| 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no | 1450.1017 | |
| Verified and Acknowledged Examiners's initials <i>M. N.</i> | | |
| TITLE : Control device for semiconductor memory device and method of controlling semiconductor memory device | | |

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

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|---|-----------|---|----------------------|
| NOTICE OF ALLOWANCE MAILED | | CLAIMS ALLOWED | |
| | | Total Claims | Print Claim for O.G. |
| ISSUE FEE | | DRAWING | |
| Amount Due | Date Paid | Sheets Drwg. | Figs. Drwg. |
| | | Print Fig. | |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | Application Examiner | |
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